

FIG. 1

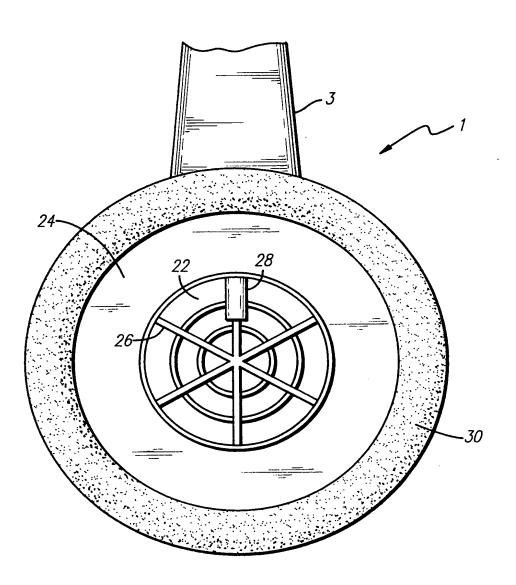


FIG. 2A

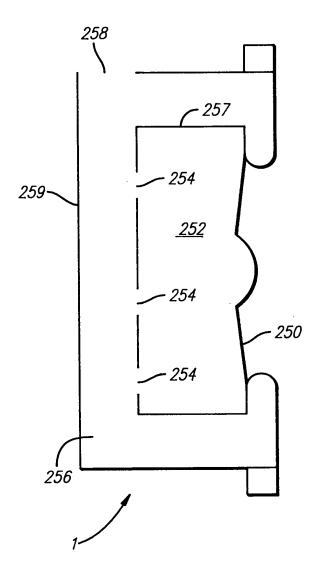
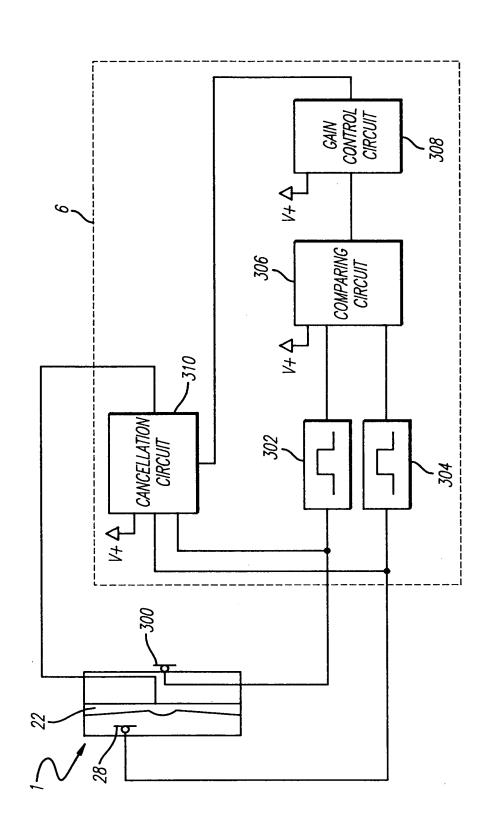
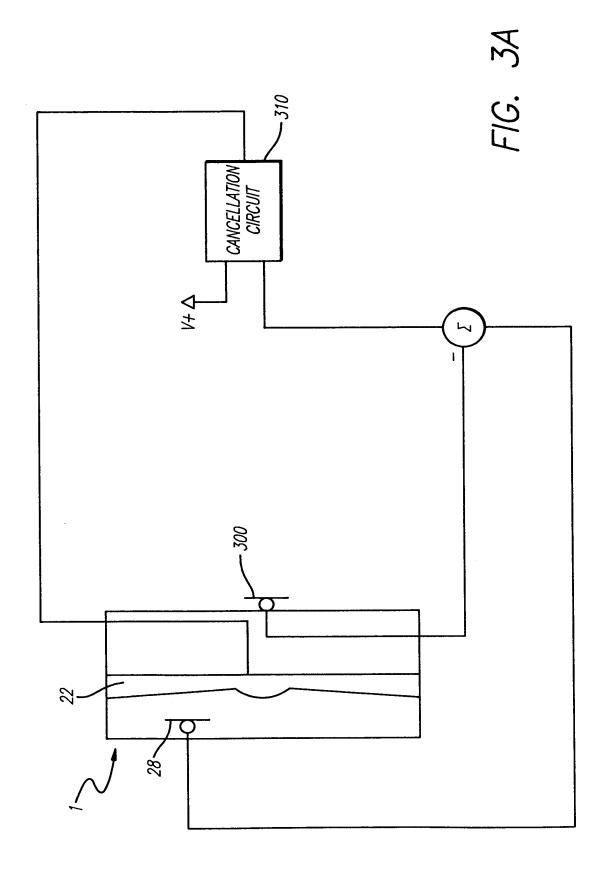


FIG. 2B

the state of the s





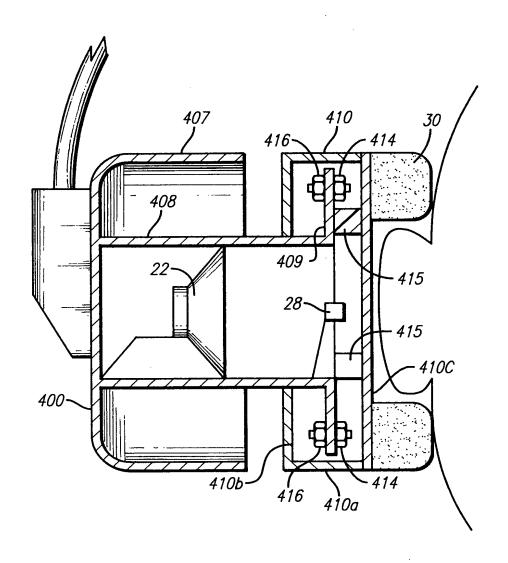
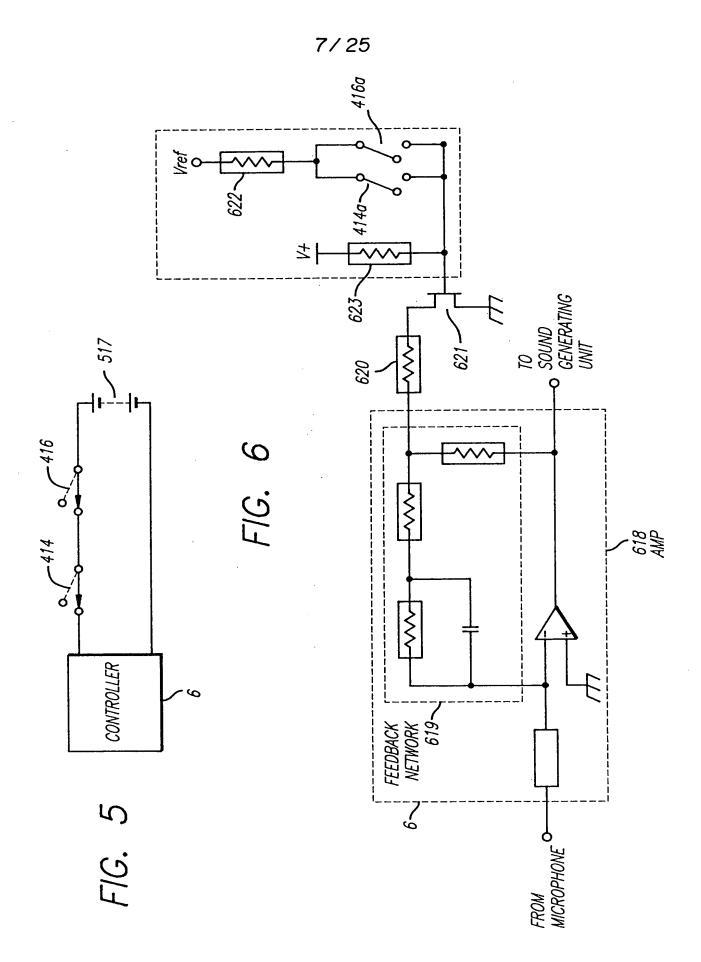
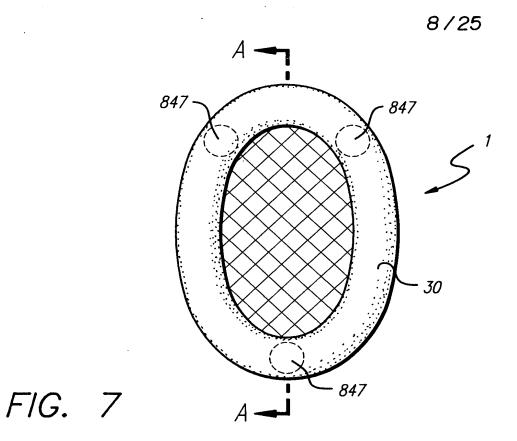


FIG. 4





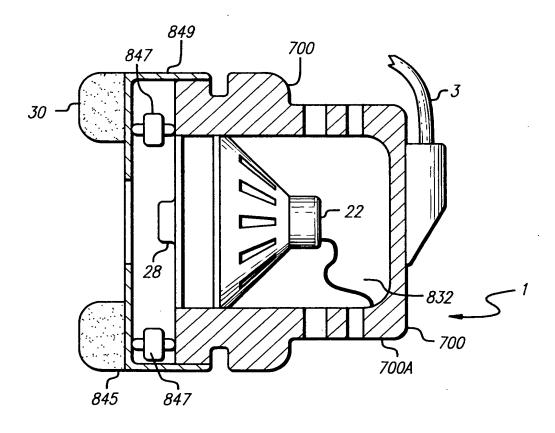
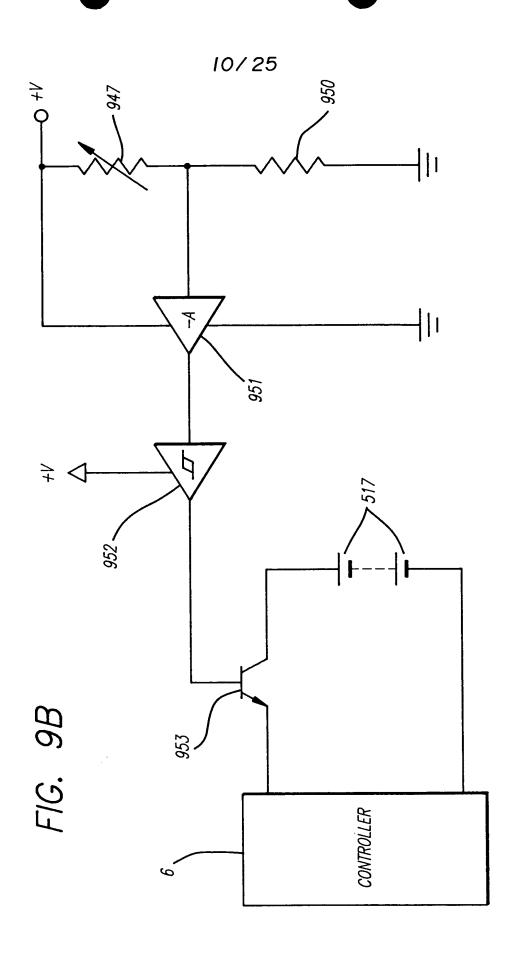
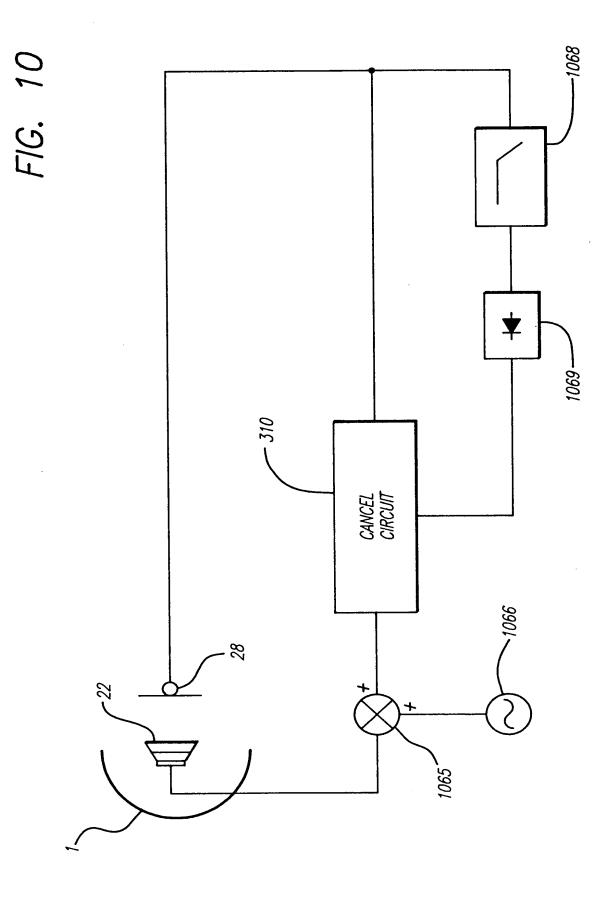


FIG. 8

9/25 - 947 950 -||1 621 - 618 -||1

F1G. 9A





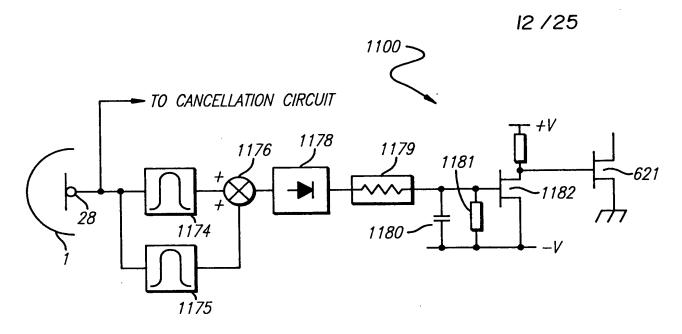


FIG. 11

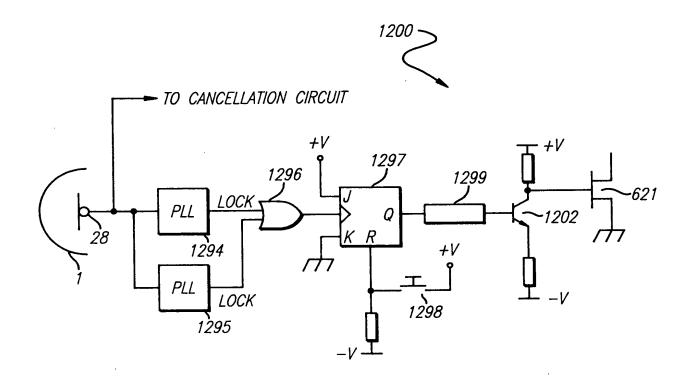


FIG. 12

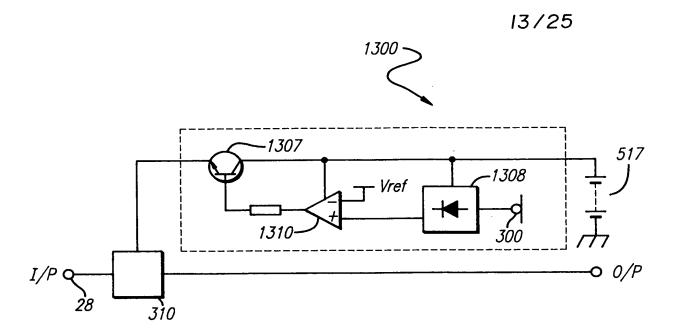


FIG. 13

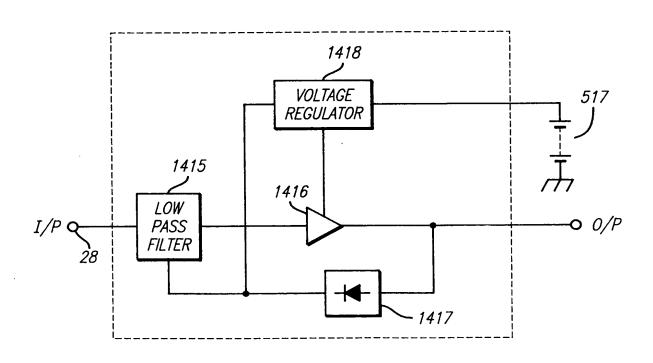
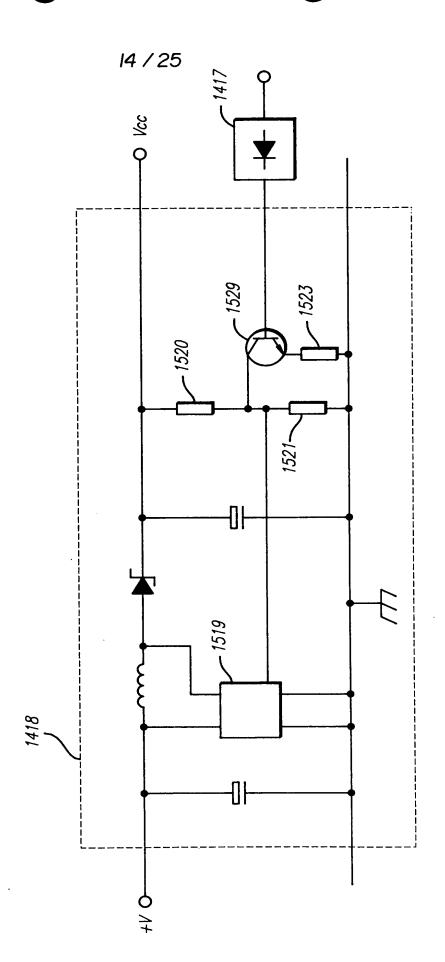
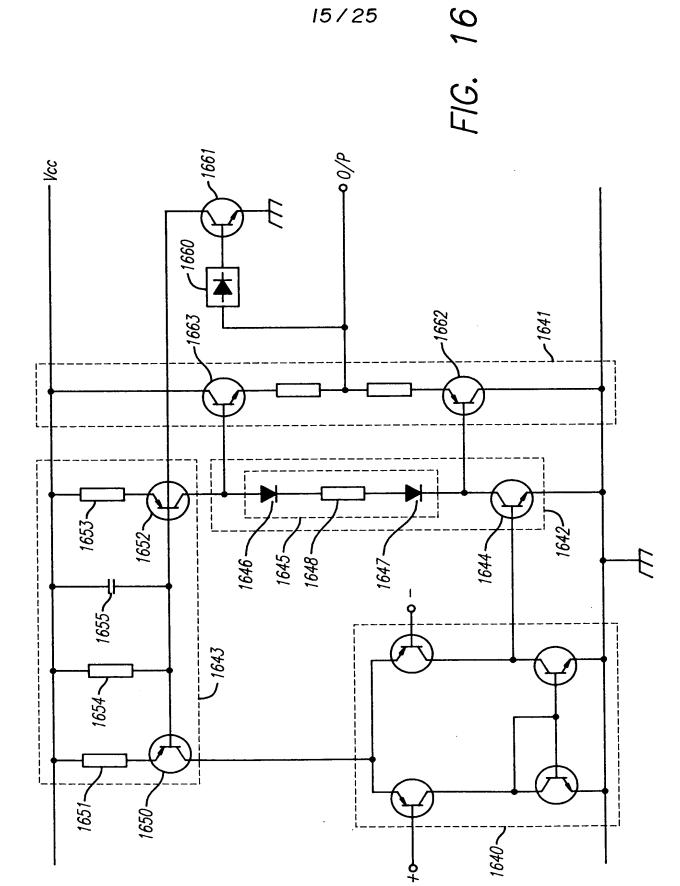
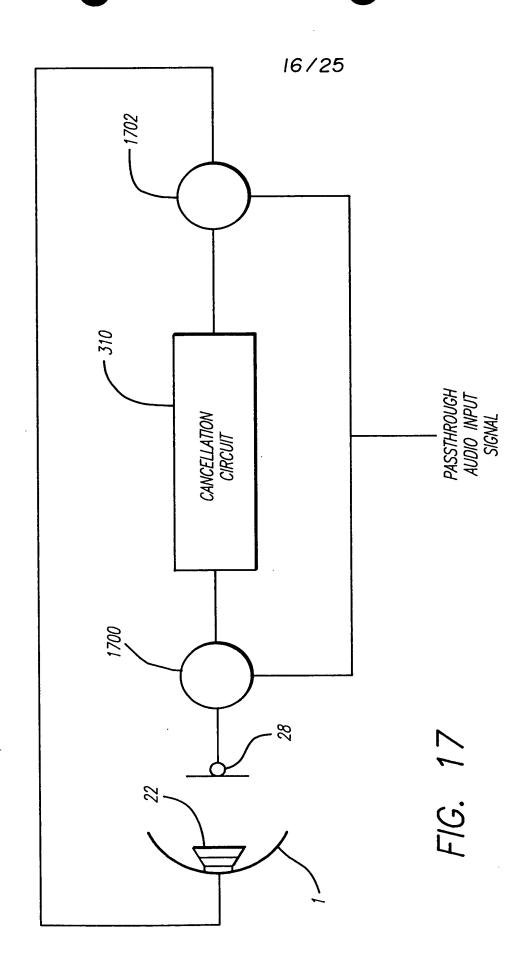


FIG. 14

FIG. 15







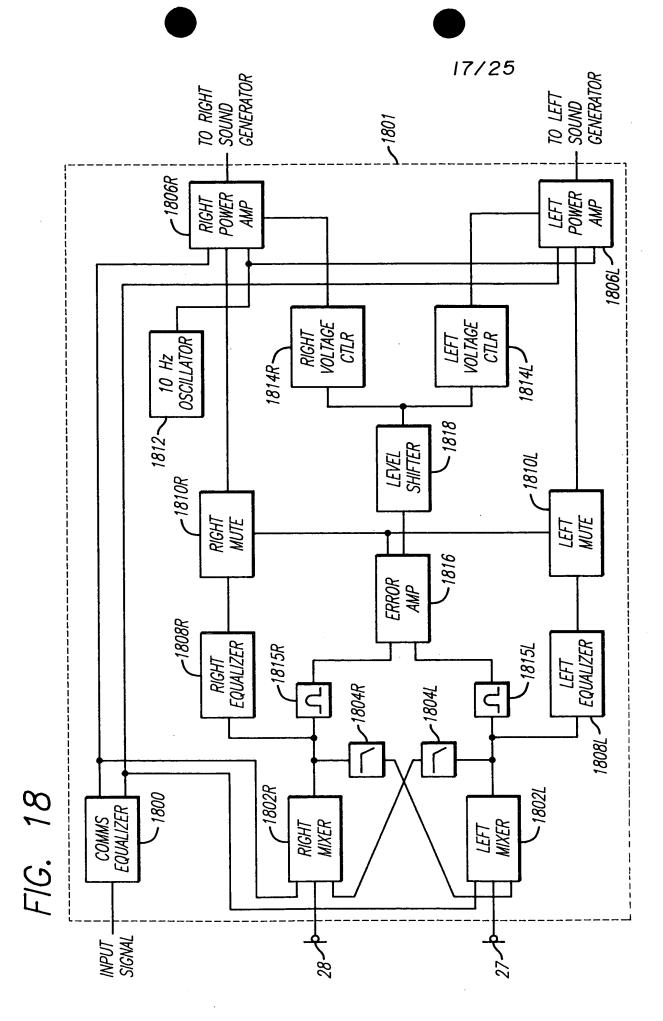
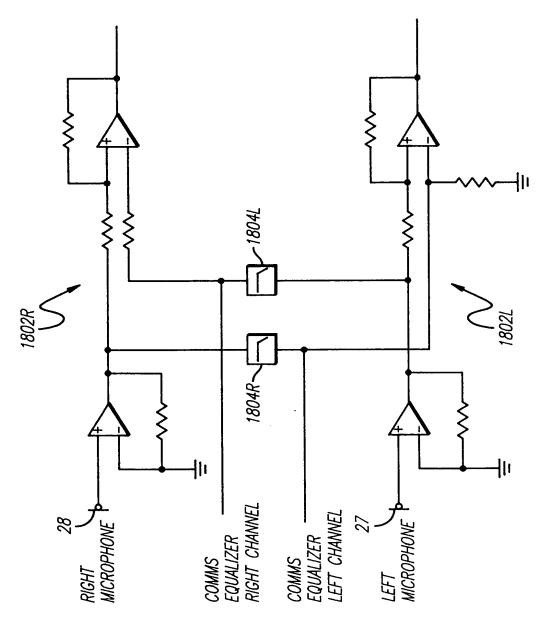


FIG. 19



19/25 - TO MUTE CIRCUIT *JJJ/+* |||| 22/1-22/1+ 1808 22/1+ FIG. 20 FROM MIXER

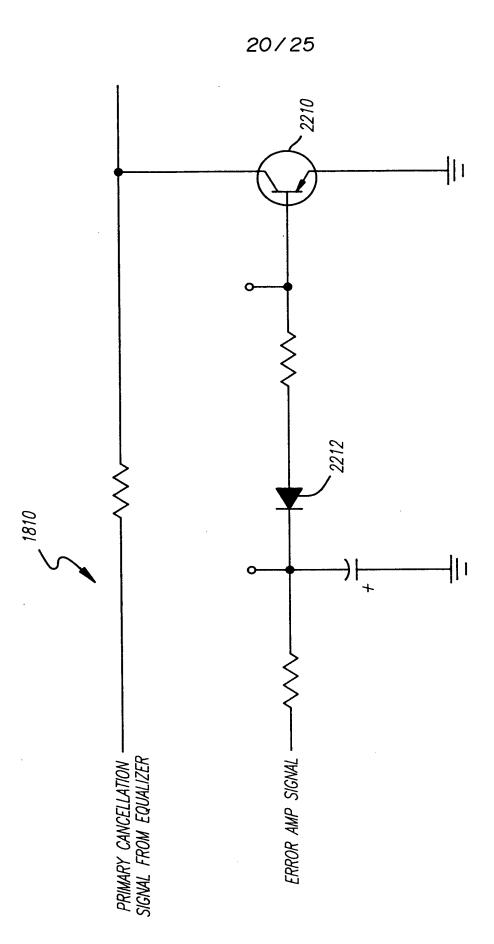
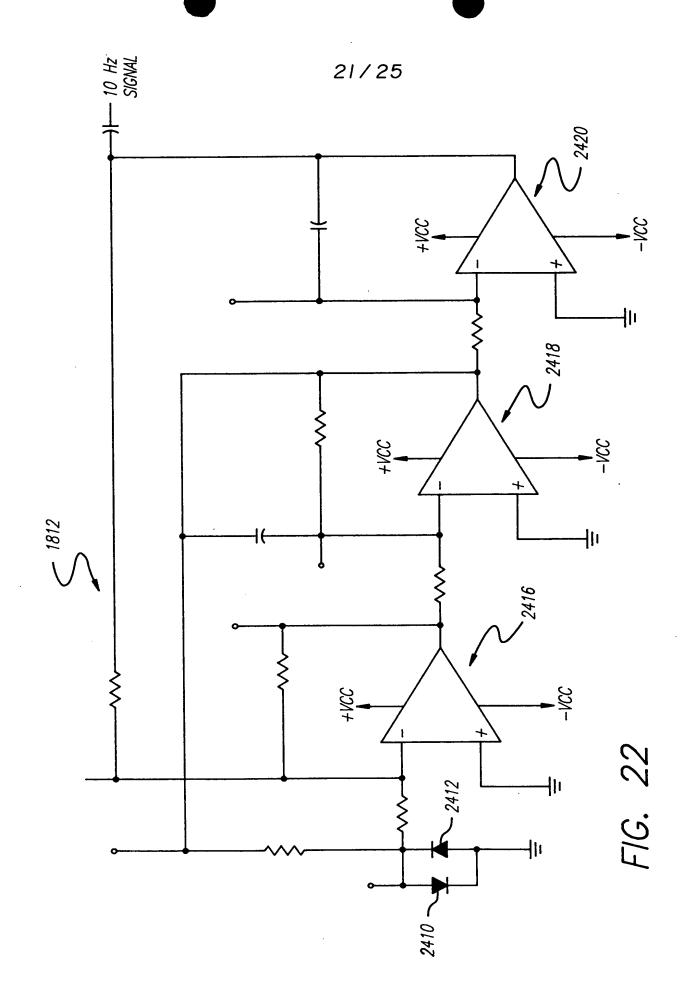
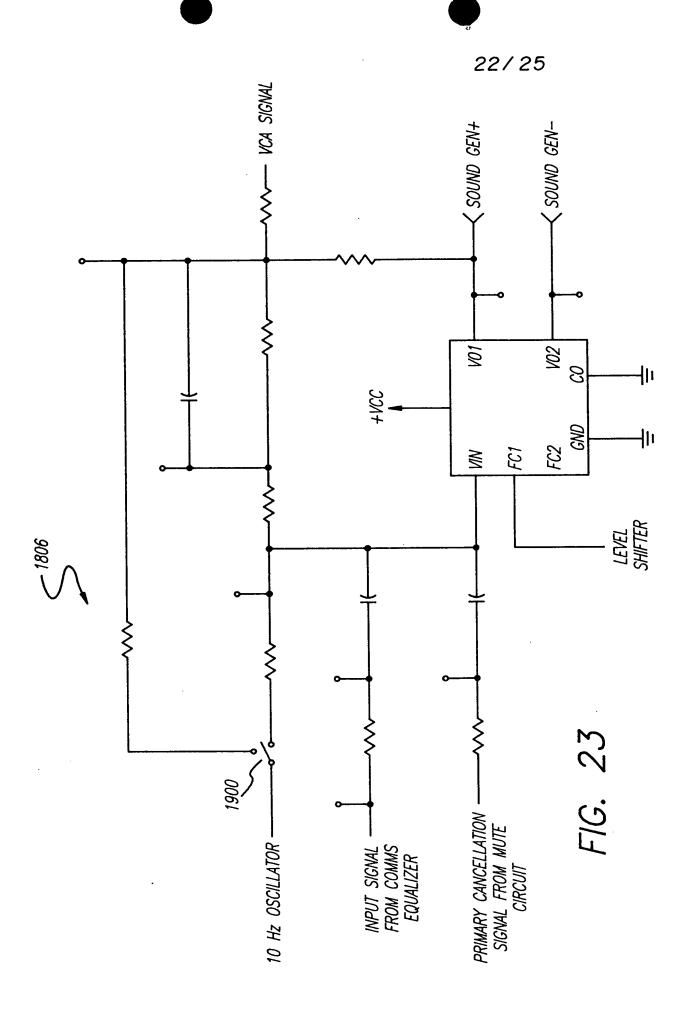
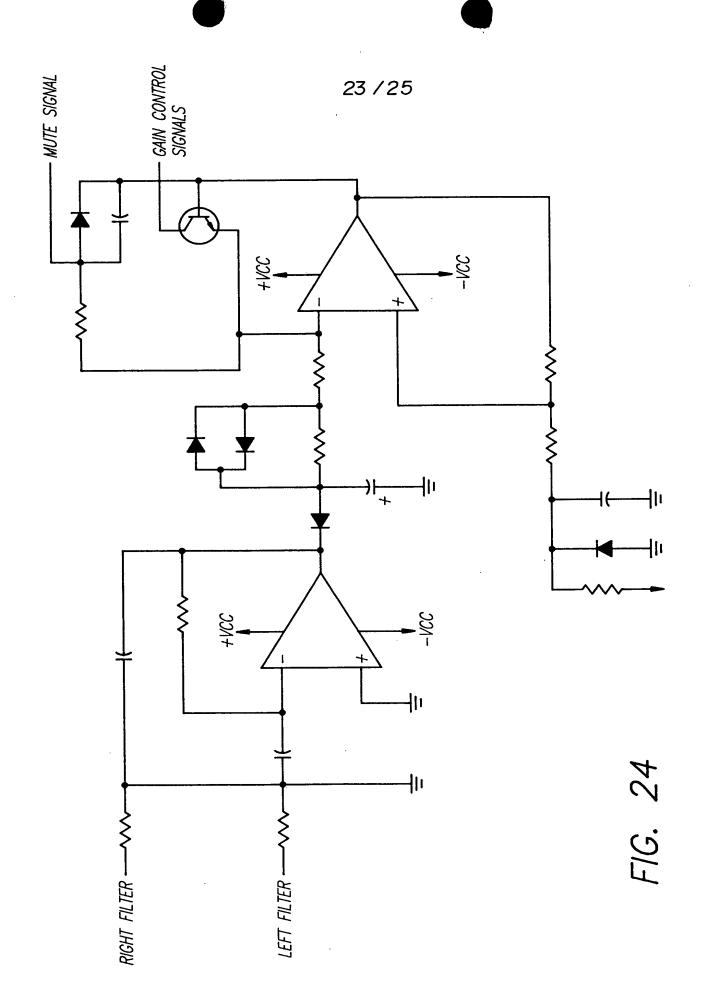
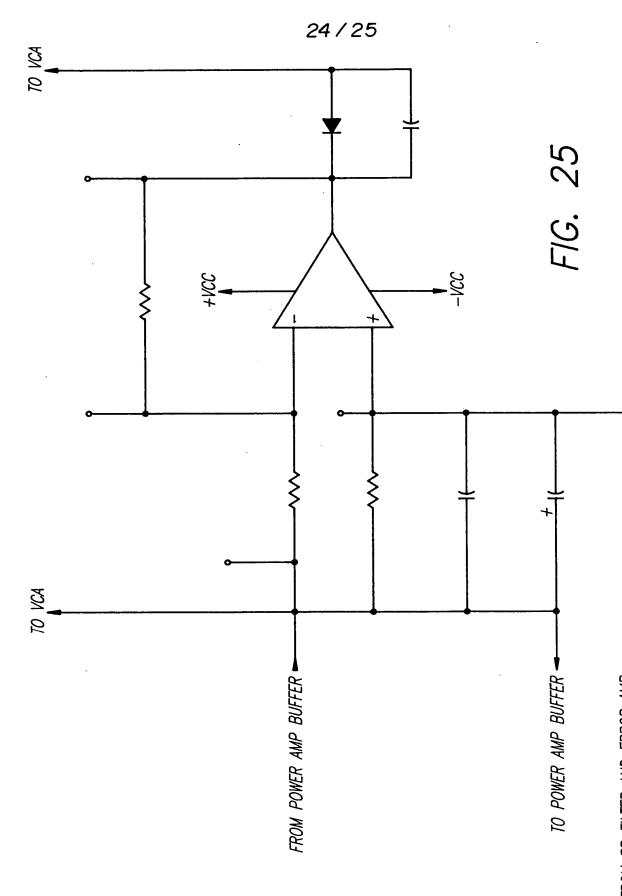


FIG. 21



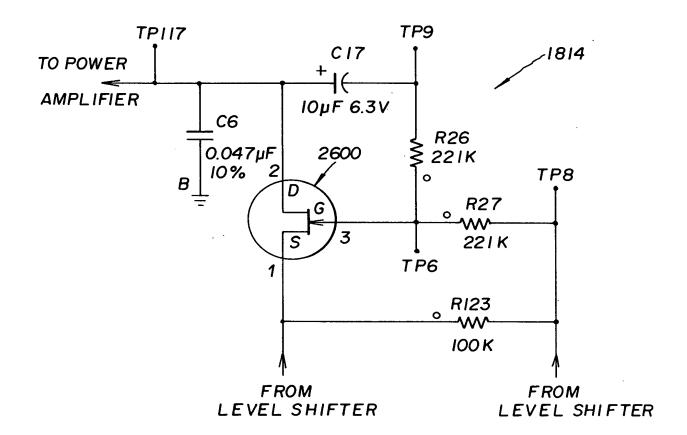






FROM BP FILTER AND ERROR AMP

25/25



F1G. 26